



Docket No.: SON-2788
(PATENT)

cof c
MT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Letters Patent of:

Confirmation No.: 8767

Katsutoshi Moriyama et al.

Patent No.: 7,130,224

Issued: October 31, 2006

For: COMPOUND STORAGE CIRCUIT AND
SEMICONDUCTOR DEVICE INCLUDING
COMPOUND STORAGE CIRCUIT

Certificate
OCT 26 2007
of Correction

REQUEST FOR CERTIFICATE OF CORRECTION

Attention: Certificate of Correction Branch
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

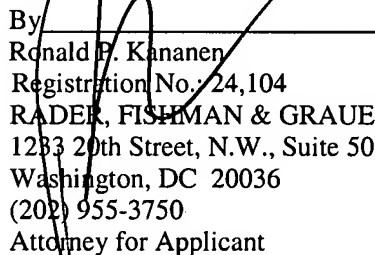
Dear Sir:

It is respectfully requested that a Certificate of Correction be issued in connection with the above-identified patent. It appears that a mistake was recorded through the fault of the Patent and Trademark Office in the printing of the patent as clearly disclosed by the records of the Office within the meaning of 35 USC § 254. Accordingly, two copies of the special Certificate of Correction are attached hereto. It is believed that the error noted is an error of consequence involving the title of the invention and thus warrants the granting of a Certificate of Correction. A copy of the Official Filing Receipt with the correct title is enclosed for the convenience of the PTO. It is believed that the error was made on the part of the PTO and that no government fee is required.

Should any costs be incurred, please consider this authorization to charge Deposit Account No. 18-0013.

Dated: October 24, 2007

Respectfully submitted,

By 
Ronald E. Kananen
Registration No.: 24,104
RADEK, FISHMAN & GRAUER PLLC
1233 20th Street, N.W., Suite 501
Washington, DC 20036
(202) 955-3750
Attorney for Applicant

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,130,224

DATED : October 31, 2006

INVENTOR(S): Katsutoshi MORIYAMA et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Title Page:

Item (54) should be read as follows:

-- COMPOUND STORAGE CIRCUIT AND SEMICONDUCTOR DEVICE INCLUDING
COMPOUND STORAGE CIRCUIT --.

MAILING ADDRESS OF SENDER:

Customer No. 23353
Rader, Fishman & Grauer PLLC
1233 20th Street, NW
Suite 501
Washington, DC 20036

PATENT NO. 7,130,224

UNITED STATES PATENT AND TRADEMARK OFFICE
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UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
 United States Patent and Trademark Office
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 P.O. Box 1450
 Alexandria, Virginia 22313-1450
 www.uspto.gov

APPL NO.	FILING OR 371(c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	TOT CLMS	IND CLMS
10/522,316	01/25/2005	2827	2110	SON-2788	18	2

23353
 RADER FISHMAN & GRAUER PLLC
 LION BUILDING
 1233 20TH STREET N.W., SUITE 501
 WASHINGTON, DC 20036

CONFIRMATION NO. 8767
 CORRECTED FILING RECEIPT



OC000000024894950

Date Mailed: 07/19/2007

Receipt is acknowledged of this nonprovisional patent application. The application will be taken up for examination in due course. Applicant will be notified as to the results of the examination. Any correspondence concerning the application must include the following identification information: the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. **If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).**

Applicant(s)

Katsutoshi Moriyama, Saga, JAPAN;
 Hironobu Mori, Nagasaki, JAPAN;
 Nobumichi Okazaki, Kanagawa, JAPAN;

Assignment For Published Patent Application

Sony Corporation, Tokyo, JAPAN

Power of Attorney: The patent practitioners associated with Customer Number 23353.

Domestic Priority data as claimed by applicant

This application is a 371 of PCT/JP03/09295 07/22/2003

Foreign Applications

JAPAN 2002-220423 07/29/2002

If Required, Foreign Filing License Granted: 05/01/2007

The country code and number of your priority application, to be used for filing abroad under the Paris Convention, is **US10/522,316**

Projected Publication Date: Not Applicable

Non-Publication Request: No

Early Publication Request: No

Title

Compound Storage Circuit and Semiconductor Device including Compound Storage Circuit

Preliminary Class

365

PROTECTING YOUR INVENTION OUTSIDE THE UNITED STATES

Since the rights granted by a U.S. patent extend only throughout the territory of the United States and have no effect in a foreign country, an inventor who wishes patent protection in another country must apply for a patent in a specific country or in regional patent offices. Applicants may wish to consider the filing of an international application under the Patent Cooperation Treaty (PCT). An international (PCT) application generally has the same effect as a regular national patent application in each PCT-member country. The PCT process **simplifies** the filing of patent applications on the same invention in member countries, but **does not result** in a grant of "an international patent" and does not eliminate the need of applicants to file additional documents and fees in countries where patent protection is desired.

Almost every country has its own patent law, and a person desiring a patent in a particular country must make an application for patent in that country in accordance with its particular laws. Since the laws of many countries differ in various respects from the patent law of the United States, applicants are advised to seek guidance from specific foreign countries to ensure that patent rights are not lost prematurely.

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Applicants may wish to consult the USPTO booklet, "General Information Concerning Patents" (specifically, the section entitled "Treaties and Foreign Patents") for more information on timeframes and deadlines for filing foreign patent applications. The guide is available either by contacting the USPTO Contact Center at 800-786-9199, or it can be viewed on the USPTO website at <http://www.uspto.gov/web/offices/pac/doc/general/index.html>.

For information on preventing theft of your intellectual property (patents, trademarks and copyrights), you may wish to consult the U.S. Government website, <http://www.stopfakes.gov>. Part of a Department of Commerce initiative, this website includes self-help "toolkits" giving innovators guidance on how to protect intellectual property in specific countries such as China, Korea and Mexico. For questions regarding patent enforcement issues, applicants may call the U.S. Government hotline at 1-866-999-HALT (1-866-999-4158).

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UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,130,224 B2
APPLICATION NO. : 10/522316
DATED : October 31, 2006
INVENTOR(S) : Katsutoshi Moriyama et al.

Page 1 of 1

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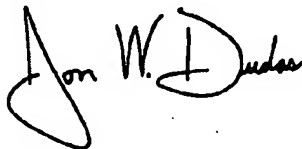
Title Page:

Item (54) should read as follows:

-- COMPOUND STORAGE CIRCUIT AND SEMICONDUCTOR DEVICE
INCLUDING COMPOUND STORAGE CIRCUIT --.

Signed and Sealed this

Tenth Day of July, 2007

A handwritten signature in black ink, appearing to read "Jon W. Dudas". The signature is stylized with a large, looped initial "J" and a cursive "Dudas".

JON W. DUDAS
Director of the United States Patent and Trademark Office